

ANALOG TO DIGITAL CONVERTER WITH INTERPOLATION OF REFERENCE LADDER

ABSTRACT OF THE DISCLOSURE

An N-bit analog to digital converter includes a reference ladder connected to an input voltage at one end, and to ground at another end, an array of differential amplifiers whose differential inputs are connected to taps from the reference ladder, wherein each amplifier has a first differential input connected to the same tap as a neighboring amplifier, and a second differential input shifted by one tap from the neighboring amplifier, and an encoder that converts outputs of the array to an N-bit output.

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